500mA / 50V Digital transistors (with built-in resistors)

DTD114EK / DTD114ES

Applications

Inverter, Interface, Driver

Feature

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on / off conditions need to be set for operation, making the device design easy.

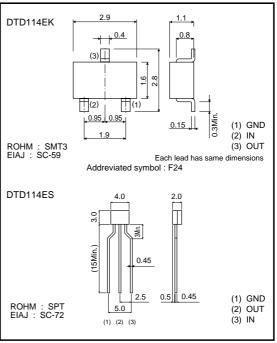
●Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

Packaging specifications

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	Package	SMT3	SPT				
	Packaging type	Taping	Taping				
	Code	T146	TP				
Part No.	Basic ordering unit (pieces)	3000	5000				
DTD114EK			_				
DTD114ES	i	-	0				

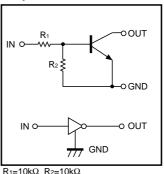
●External dimensions (Unit: mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits		Unit			
	Symbol	DTD114EK	TD114EK DTD114ES				
Supply voltage	Vcc	50		V			
Input voltage	Vin	-10 to +40		V			
Output current	Ic	500		mA			
Power dissipation	Po	200 300		mW			
Junction temperature	Tj	150		Ĵ			
Storage temperature	Tstg	-55 to +150		°C			

●Equivalent circuit



Rev.A

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	V _{I(off)}	_	_	0.5	V	Vcc=5V, Io=100μA
	V _{I(on)}	3	_	_]	Vo=0.3V, Io=10mA
Output voltage	Vo(on)	-	0.1	0.3	V	Io/I:=50mA/2.5mA
Input current	lı	-	_	0.88	mA	V:= 5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V
DC current gain	Gı	56	-	-	_	Vo=5V, Io=50mA
Input resistance	R ₁	7	10	13	kΩ	-
Resistance ratio	R ₂ /R ₁	0.8	1	1.2	_	-
Transition frequency	f⊤ *	_	200	_	MHz	VcE=10V, IE=-50mA, f=100MHz

^{*} Characteristics of built-in transistor

•Electrical characteristics curves

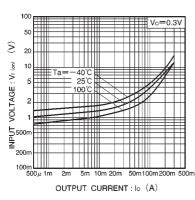


Fig.1 Input voltage vs. output current (ON characteristics)

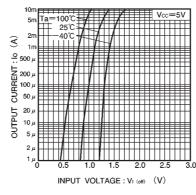


Fig.2 Output current vs. input voltage (OFF characteristics)

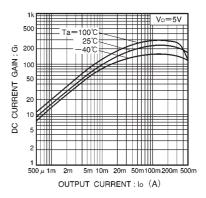


Fig.3 DC current gain vs. output current

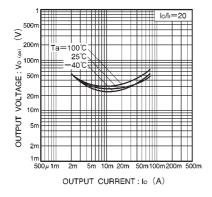


Fig.4 Output voltage vs. output current

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